



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re DIVISIONAL Application of)
Shunpei YAMAZAKI et al.)
Based On Serial No. 08/784,290) Art Unit: 2823
Which Was Filed: January 16, 1997) Examiner: B. Dutton
For: METHOD FOR FABRICATING)
A SEMICONDUCTOR DEVICE) Date: October 31, 2000

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 08/784,290 filed January 16, 1998.

Respectfully submitted,

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